



TSF10N80M

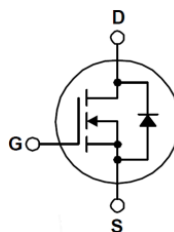
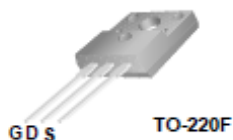
800V N-Channel MOSFET

General Description

This Power MOSFET is produced using Truesemi's advanced planar stripe DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

Features

- 10.0A,800V,Max. $R_{DS(on)}$ =1.10 Ω @ V_{GS} =10V
- Low gate charge(typical 45nC)
- High ruggedness
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



Absolute Maximum Ratings

$T_C=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter		Value	Units
V_{DSS}	Drain-Source Voltage		800	V
V_{GS}	Gate-Source Voltage		± 30	V
I_D	Drain Current	$T_C = 25^{\circ}\text{C}$	10.0	A
		$T_C = 100^{\circ}\text{C}$	6.0	A
I_{DM}	Pulsed Drain Current	(Note 1)	40	A
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	920	mJ
E_{AR}	Repetitive Avalanche Energy	(Note 1)	24	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.0	V/ns
P_D	Power Dissipation		48	W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^{\circ}\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	$^{\circ}\text{C}$

* Drain current limited by maximum junction temperature.

Thermal Resistance Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Thermal Resistance,Junction-to-Case	--	2.6	$^{\circ}\text{C/W}$
$R_{\theta CS}$	Thermal Resistance,Case-to-Sink Typ.	0.24	--	$^{\circ}\text{C/W}$
$R_{\theta JA}$	Thermal Resistance,Junction-to-Ambient	--	62.5	$^{\circ}\text{C/W}$

Electrical Characteristics $T_C=25\text{ }^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	3.0	--	5.0	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 5.0\text{ A}$	--	0.93	1.10	Ω

Off Characteristics

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	800	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\text{ }\mu\text{A}$, Referenced to $25\text{ }^{\circ}\text{C}$	--	0.99	--	$\text{V}/^{\circ}\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 800\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 640\text{ V}, T_C = 125\text{ }^{\circ}\text{C}$	--	--	10	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	2600	--	pF
C_{oss}	Output Capacitance		--	192	--	pF
C_{rss}	Reverse Transfer Capacitance		--	25	--	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Time	$V_{DS} = 400\text{ V}, I_D = 10.0\text{ A},$ $R_G = 25\text{ }\Omega$ (Note 4,5)	--	50	--	ns
t_r	Turn-On Rise Time		--	130	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	100	--	ns
t_f	Turn-Off Fall Time		--	80	--	ns
Q_g	Total Gate Charge	$V_{DS} = 640\text{ V}, I_D = 10.0\text{ A},$ $V_{GS} = 10\text{ V}$ (Note 4,5)	--	45	--	nC
Q_{gs}	Gate-Source Charge		--	14	--	nC
Q_{gd}	Gate-Drain Charge		--	18	--	nC

Source-Drain Diode Maximum Ratings and Characteristics

I _S	Continuous Source-Drain Diode Forward Current		--	--	10.0	A
I _{SM}	Pulsed Source-Drain Diode Forward Current		--	--	40.0	
V _{SD}	Source-Drain Diode Forward Voltage	I _S = 10.0A, V _{GS} = 0 V	--	--	1.5	V
t _{rr}	Reverse Recovery Time	I _S =10.0A, V _{GS} = 0 V di _F /dt = 100 A/μs (Note 4)	--	730	--	ns
Q _{rr}	Reverse Recovery Charge		--	11	--	μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L=17.5\text{ mH}, I_{AS}=10.0\text{ A}, V_{DD}=50\text{ V}, R_G=25\text{ }\Omega$, Starting $T_J=25\text{ }^{\circ}\text{C}$
3. $I_{SD}\leq 10.0\text{ A}, di/dt \leq 200\text{ A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25\text{ }^{\circ}\text{C}$
4. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Characteristics

Fig. 1 $I_D - V_{DS}$

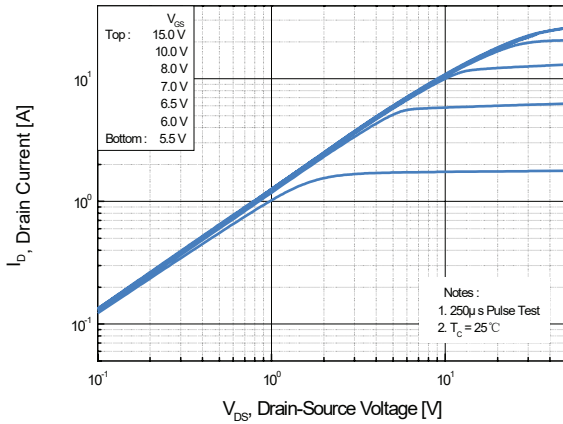


Fig. 2 $I_D - V_{GS}$

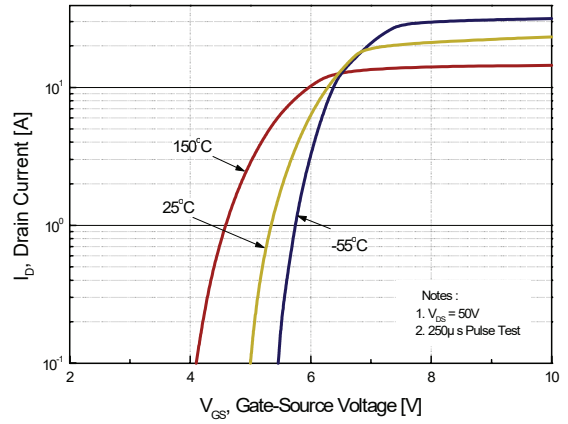


Fig. 3 $R_{DS(ON)} - I_D$

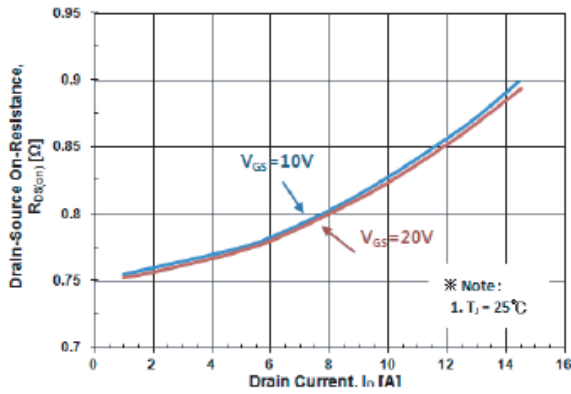


Fig. 4 $I_S - V_{SD}$

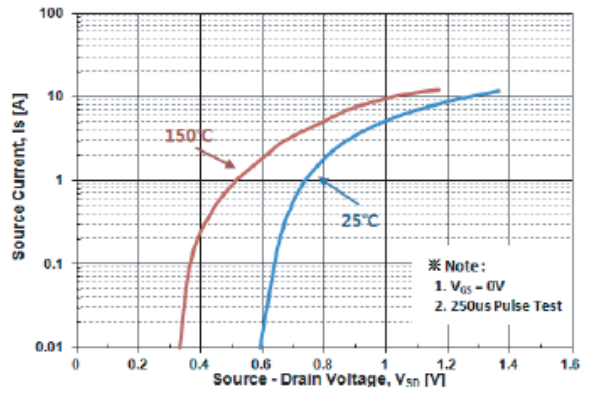


Fig. 5 Capacitance - V_{DS}

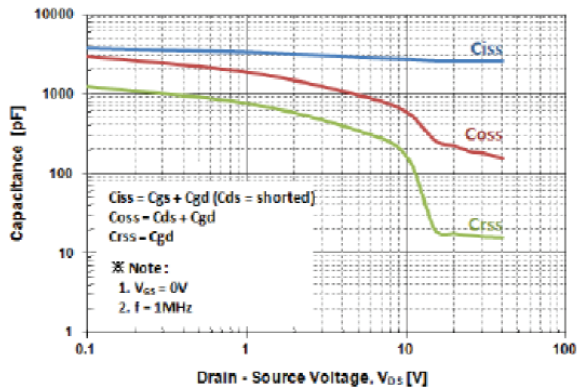
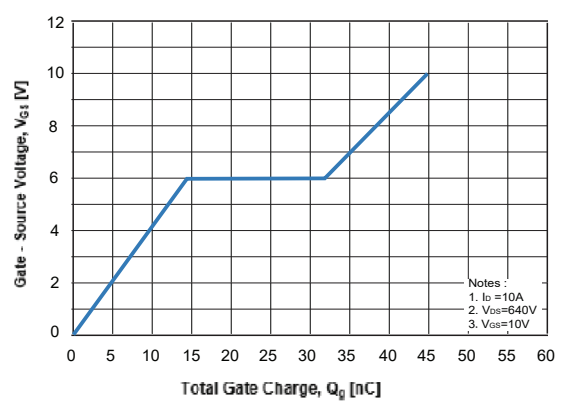


Fig. 6 $V_{GS} - Q_G$



Typical Characteristics

Fig. 7 $V_{DS} - T_J$

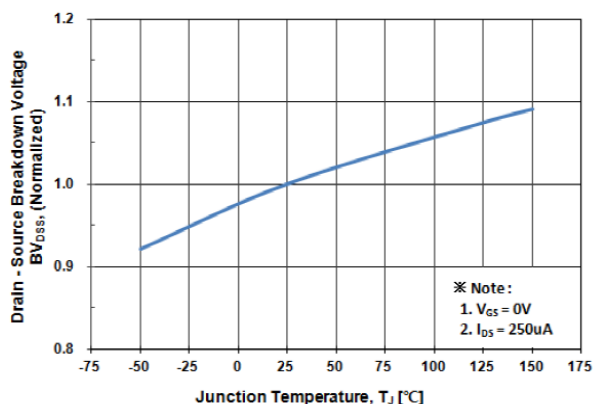


Fig. 8 $R_{DS(on)} - T_J$

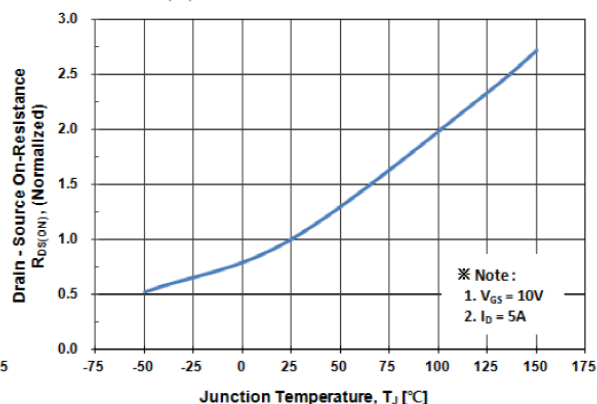


Fig. 9 $I_D - T_C$

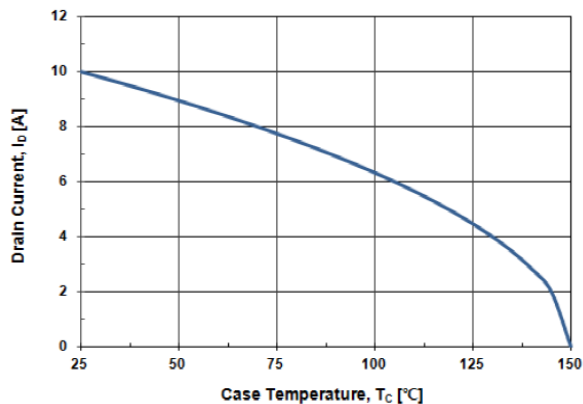


Fig. 10 Safe Operating Area

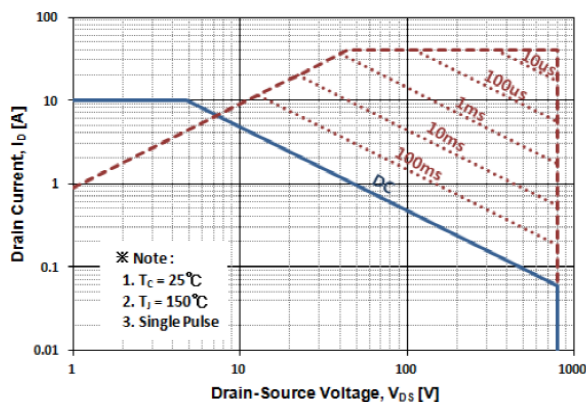


Fig. 11 Transient Thermal Impedance

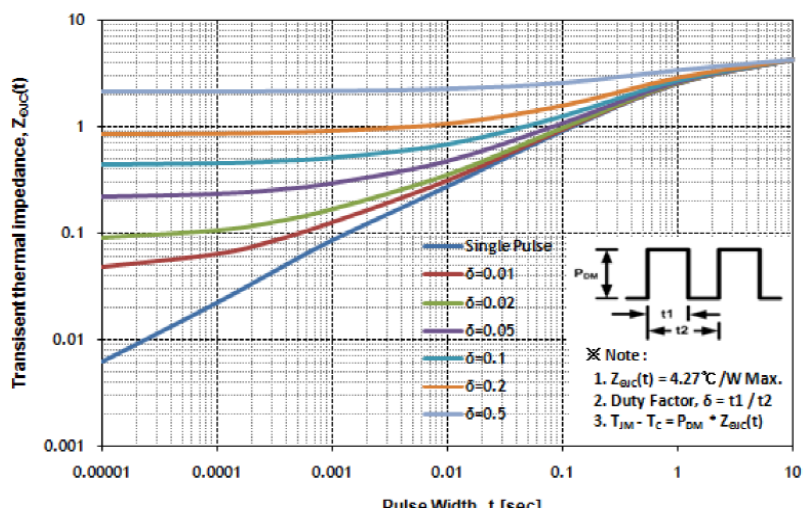


Fig. 12 Gate Charge Test Circuit & Waveform

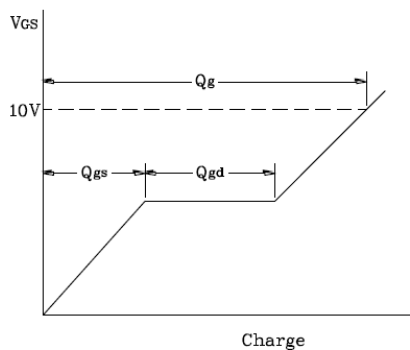
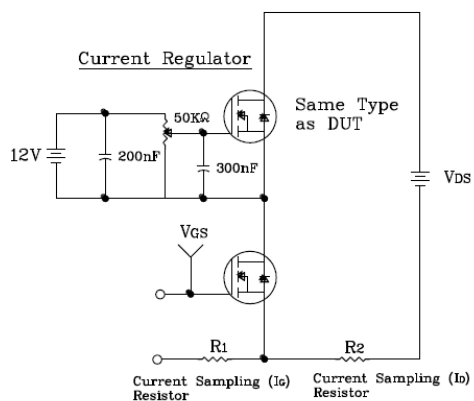


Fig. 13 Resistive Switching Test Circuit & Waveform

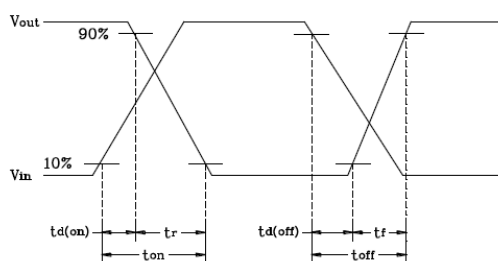
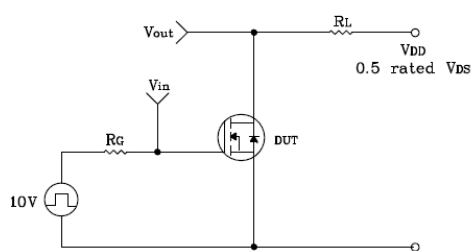


Fig. 14 E_{AS} Test Circuit & Waveform

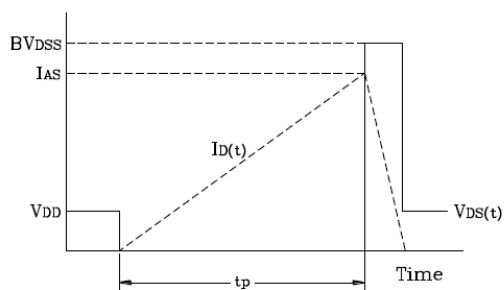
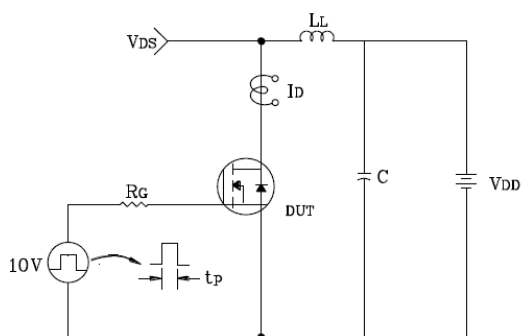


Fig. 15 Diode Reverse Recovery Time Test Circuit & Waveform

